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a first thumbnail writing circuit for writing to said buffer thumbnail image data outputted from said processor; and

a second thumbnail writing circuit for writing to said main memory thumbnail image data stored in said buffer each time a first predetermined lines of original image data is written to said main memory.--

--14. A digital camera according to claim 13, wherein

a second predetermines lines of thumbnail image data is associated with said first predetermined lines of original image data, and

said buffer including a thumbnail area to store said second predetermined lines of thumbnail image data.--

--15. A digital camera according to claim 13, further comprising a horizontal counter to count a horizontal number of pixels of said original image data and output a horizontal count value, and

a vertical counter to count a vertical number of lines of said original image data and output a vertical count value.--

--16. A digital camera according to claim 15, wherein

said processor includes an extracting circuit to extract predetermined pixel data from said original image data based on said horizontal count value and said vertical count value.--

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CONT'D

--17. A digital camera according to claim 16, wherein
said extracting circuit includes a plurality of registers to shift said original image
data by a predetermined number of pixels a time and a register controller to intermittently enable
said registers based on said horizontal count value and said vertical count value.--

--18. A digital camera according to claim 15, wherein
said first thumbnail writing circuit includes a data writing circuit to write said
thumbnail image data to said buffer based on said horizontal count value and said vertical count
value.--

--19. A digital camera according to claim 15, wherein
said first thumbnail writing circuit includes a request output circuit to output a
request or read out said thumbnail image data when said vertical counter counts up the number
of lines corresponding to said first predetermined lines and said horizontal counter counts up the
horizontal number of pixels on said original image data.--

--20. A digital camera according to claim 19, wherein
said second thumbnail writing includes a thumbnail reading circuit to read said
thumbnail image data out of said buffer in response to said read request.--

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AI
CONT'D

--21. A digital camera according to claim 13, wherein

said original writing circuit includes a first original writing circuit to write said original image data by a predetermined number of pixels a time, and a second original writing circuit to write original image data stored in said buffer to said main memory by said predetermined number of pixels a time.--

--22. A digital camera according to claim 21, wherein

said buffer includes an original area to store at least said predetermined number of pixels of original image data.--

--23. A digital camera according to claim 13, wherein

said main memory is an SDRAM.--

--24. A digital camera according to claim 13, further comprising a recorder to record

original image data and thumbnail image data stored in said main memory to a recording medium.--

--25. A digital camera, comprising:

a picture taking means for taking a subject and outputting original image data by a raster scan scheme;
a main memory;

an original writing means for writing said original image data to said main memory;

a creating means for creating a thumbnail image data based on said original image data;

a buffer;

a first thumbnail writing means for writing to said buffer thumbnail image data outputted from said creating means; and

a second thumbnail writing means for writing to said main memory thumbnail image data stored in said buffer each time a first predetermined lines of original image data is written to said main memory.--

--26. A digital camera according to claim 25, wherein

a second predetermines lines of thumbnail image data is associated with said first predetermined lines of original image data, and

said buffer including a thumbnail area to store said second predetermined lines of thumbnail image data.--

--27. A digital camera according to claim 25, further comprising a horizontal counter to count a horizontal number of pixels of said original image data and output a horizontal count value, and

a vertical counter to count a vertical number of lines of said original image data and output a vertical count value.--

--28. A digital camera according to claim 27, wherein
said creating means includes an extracting means to extract predetermined pixel
data from said original image data based on said horizontal count value and said vertical count
value.--

--29. A digital camera according to claim 28, wherein

--30. A digital camera according to claim 27, wherein

--31. A digital camera according to claim 27, wherein

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--32. A digital camera according to claim 31, wherein

said second thumbnail writing means includes a thumbnail reading means to read said thumbnail image data out of said buffer in response to said read request.--

--33. A digital camera according to claim 25, wherein

said original writing means includes a first original writing means to write said original image data by a predetermined number of pixels a time, and a second original writing means to write original image data stored in said buffer to said main memory by said predetermined number of pixels a time.--

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--34. A digital camera according to claim 33, wherein

said buffer includes an original area to store at least said predetermined number of pixels of original image data.--

--35. A digital camera according to claim 25, wherein

said main memory is an SDRAM.--

--36. A digital camera according to claim 25, further comprising a recording means to

record original image data and thumbnail image data stored in said main memory to a recording medium.--